

Notice of References Cited	Application/Control No. 10/051,280	Applicant(s)/Patent Under Reexamination SHIMBO, ATSUSHI	
	Examiner Chat C. Do	Art Unit 2124	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,557,020	04-2003	Amano et al.	708/491
	B	US-6,366,940	04-2002	Ono et al.	708/491
	C	US-4,366,549	12-1982	Katayama, Aisuke	708/491
	D	US-5,077,793	12-1991	Falk et al.	708/491
	E	US-6,148,034	11-2000	Lipovski, G. Jack	708/491
	F	US-6,240,436	05-2001	McGregor, Matthew Scott	708/491
	G	US-6,434,585	08-2002	McGregor et al.	708/491
	H	US-6,662,201	12-2003	Kawamura, Shinichi	708/491
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Jean-Claude et al., An RNS Montgomery Modular Multiplication Algorithm, 1998, IEEE Transactions on Computers, Vol. 47, No. 7, pages 766-776.
	V	Kenneth et al., The Use of Residue Number Systems in the Design of Finite Impulse Response Digital Filters, 1977, IEEE Transactions on Circuits and Systems, Vol. CAS-24, No. 4, pages 191-201.
	W	Jean-Claude et al., Modular Multiplication and Base Extensions in Residue Number Systems, 2001, IEEE, pages 59-65.
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.